

APPLICATION DATA SHEET
(Inventor(s) With Representation)

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Application Information

Title Line One::	Signal Interconnect Incorporating Multiple
Title Line Two::	Modular Units
Total Drawing Sheets::	4
Formal Drawings?	No
Application Type::	Utility
Docket Number::	TEL4597P0061US

Representative Information

Registration Number One::	29,116
Registration Number Two::	35,997
Registration Number Three::	39,135
Registration Number Four::	

Continuity Information

This application is a::	Non Prov. of Provisional
>Application One::	60/222,352
Filing Date::	08/01/00

which is a::
>>Application Two::
Filing Date::

which is a::
>>>Application Three::
Filing Date::

Prior Foreign Applications

Foreign Application One::
Filing Date::
Country::
Priority Claimed::

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**Box PATENT APPLICATION
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Washington, D.C. 20231**

Case Number: TEL4597P0061US

Serial No. New application

Filing Date: March 14, 2001

Inventors: Philip J. Lin

For: "Signal Interconnect Incorporating Multiple Modular Units"

Enclosed are the following documents:

Certificate of Mailing by Express Mail; Application Data Sheet; Utility Patent Application Transmittal (3 pages) with our Check No. *40446* for \$1758.00 attached; Specification (8 pages), claims (7 pages - 56 claims), Abstract of the Disclosure (1 page); 4 sheets of informal drawings; Declaration and Power of Attorney executed (*3* pages); Recordation Form cover Sheet Patents Only with two-page Assignment attached and our Check No. *40448* for \$40.00; Information Disclosure Statement with copies of documents attached; and a prepaid post card for acknowledgment of receipt by the mail room of the USPTO.



Signature of person mailing documents